

### **ABSTRACT OF THE DISCLOSURE**

A guaranteed two-wire interface in which upon determining that an operation is to be performed on a slave component, a master component transmits at least a portion of a corresponding frame to the slave component over the data wire. The master component repeats this detection and transmission each time it determines that an operation is to be performed on a slave component, whether the same component or a different slave component as the slave component previously communicated with. The frame structure itself may change depending on the operation to be performed. For example, one frame might include an extended address data field that includes extended address information that goes beyond a basic address field included in another frame. One frame may include reliability fields (such as cyclic redundancy checking field, an acknowledgement field, and/or an error field), whereas another frame having a different operation may not.

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WORKMAN NYDEGGER  
A PROFESSIONAL CORPORATION  
ATTORNEYS AT LAW  
1000 EAGLE GATE TOWER  
60 EAST SOUTH TEMPLE  
SALT LAKE CITY, UTAH 84111